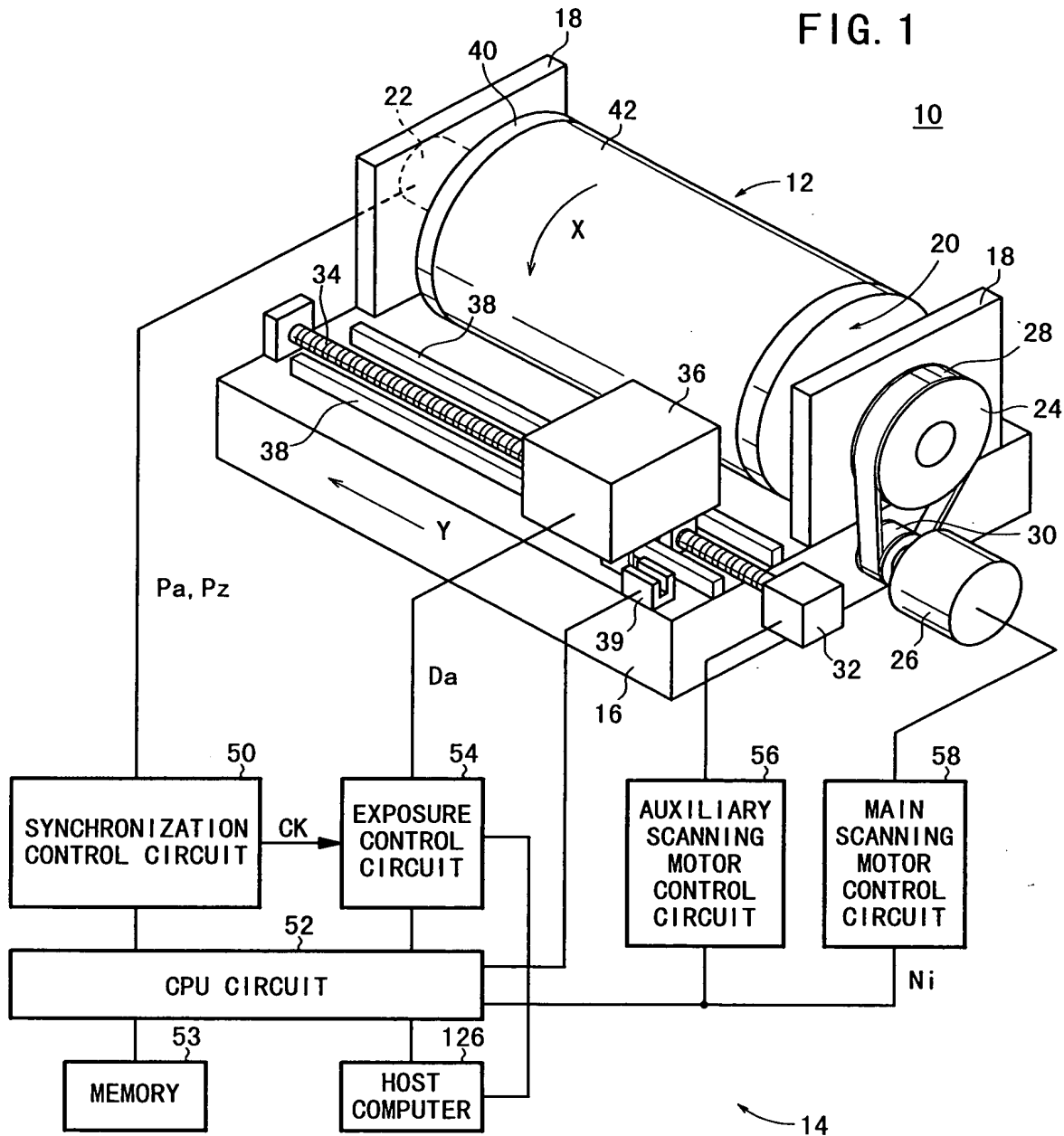


FIG. 1



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FIG. 2

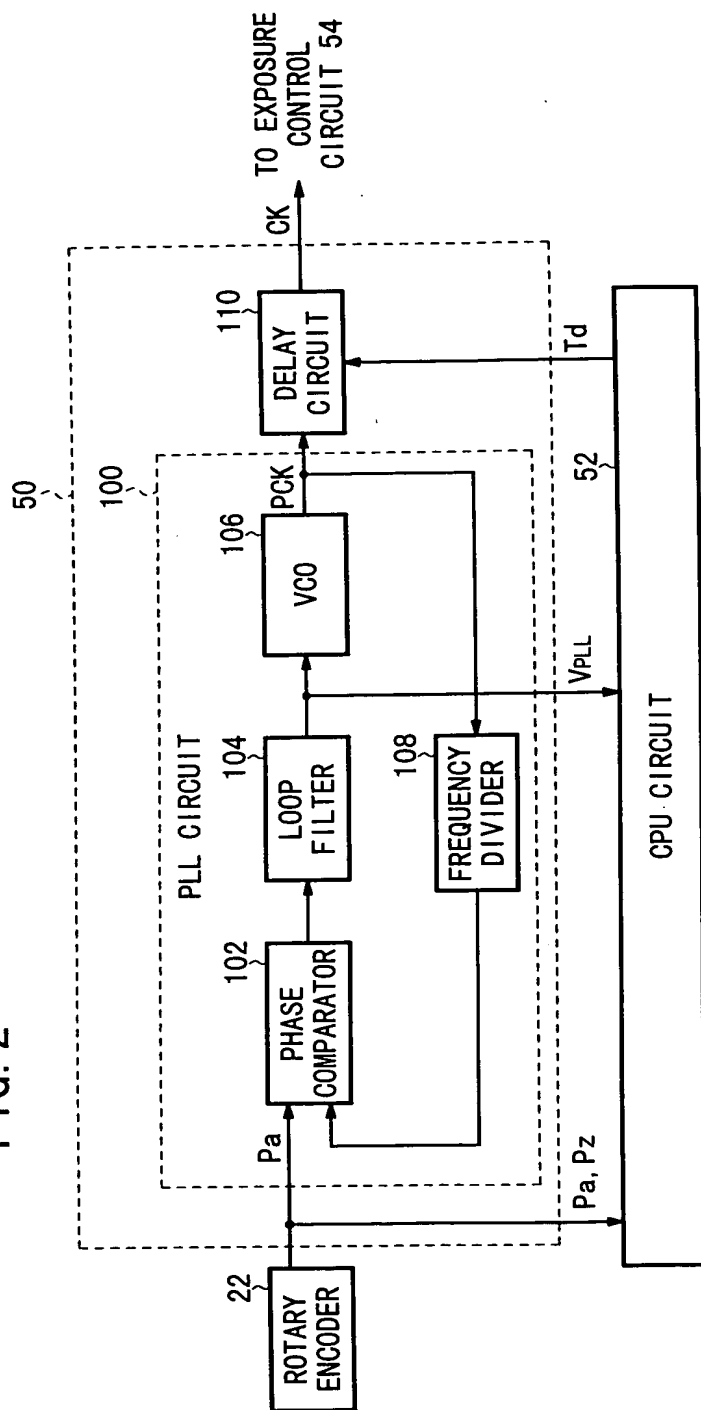


FIG. 3

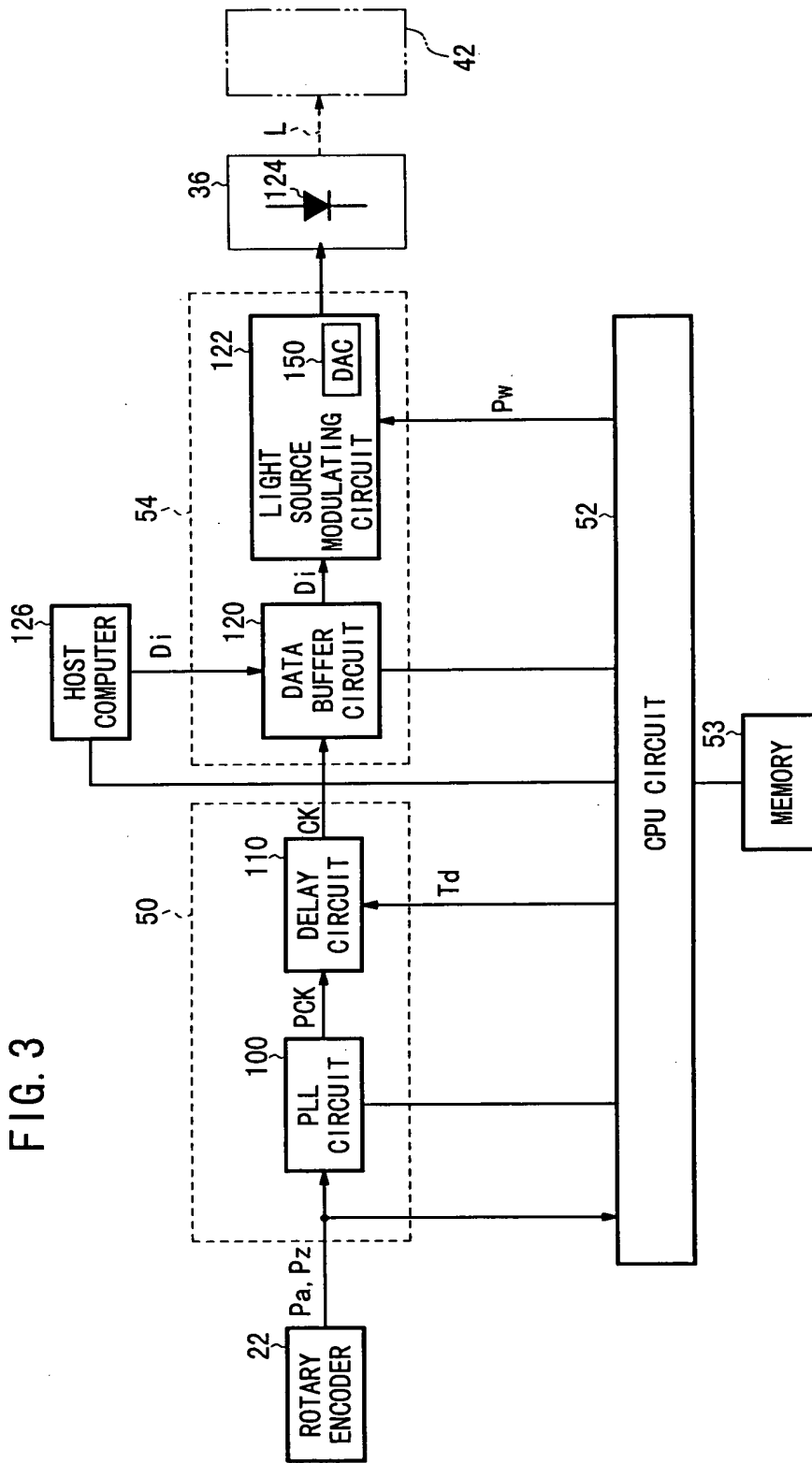


FIG. 4A

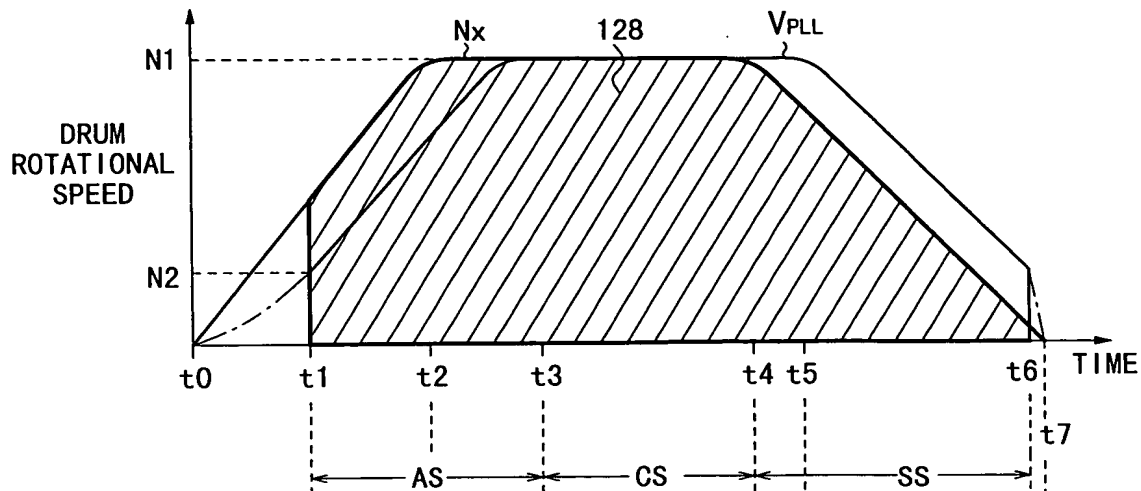


FIG. 4B

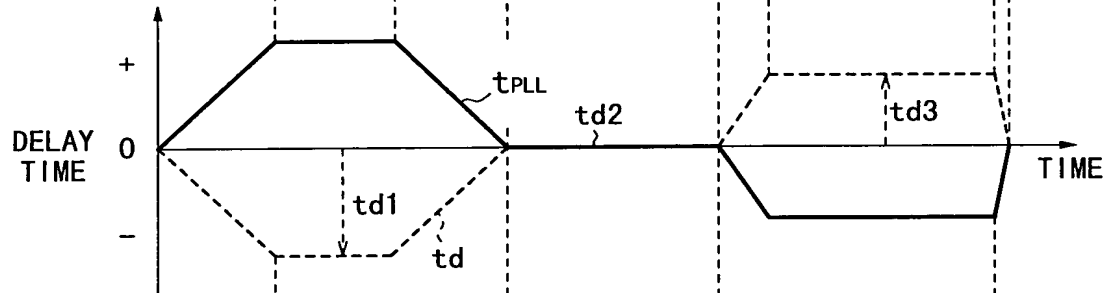
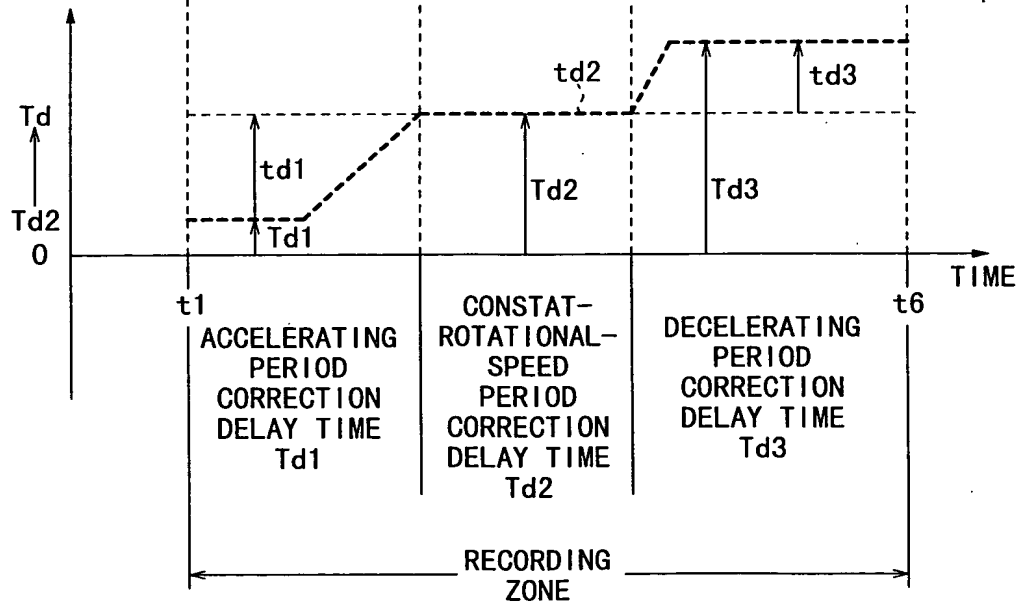


FIG. 4C



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FIG. 5

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STORAGE AREA		ADDRESS	DATA
53a	BASIC DELAY TIME DATA td	ADa   ADb	td1   td2   td3
53b	ACCELERATING PERIOD CORRECTION DELAY TIME	ADc   ADd	Td1   Td2
53c	DECELERATING PERIOD CORRECTION DELAY TIME	ADe   ADf	Td2   Td3
53d	ACCELERATING PERIOD LASER OUTPUT	ADg   ADh	Pwa   Pwc
53e	DECELERATING PERIOD LASER OUTPUT	ADi   ADj	Pwc   Pws
⋮			

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FIG. 6

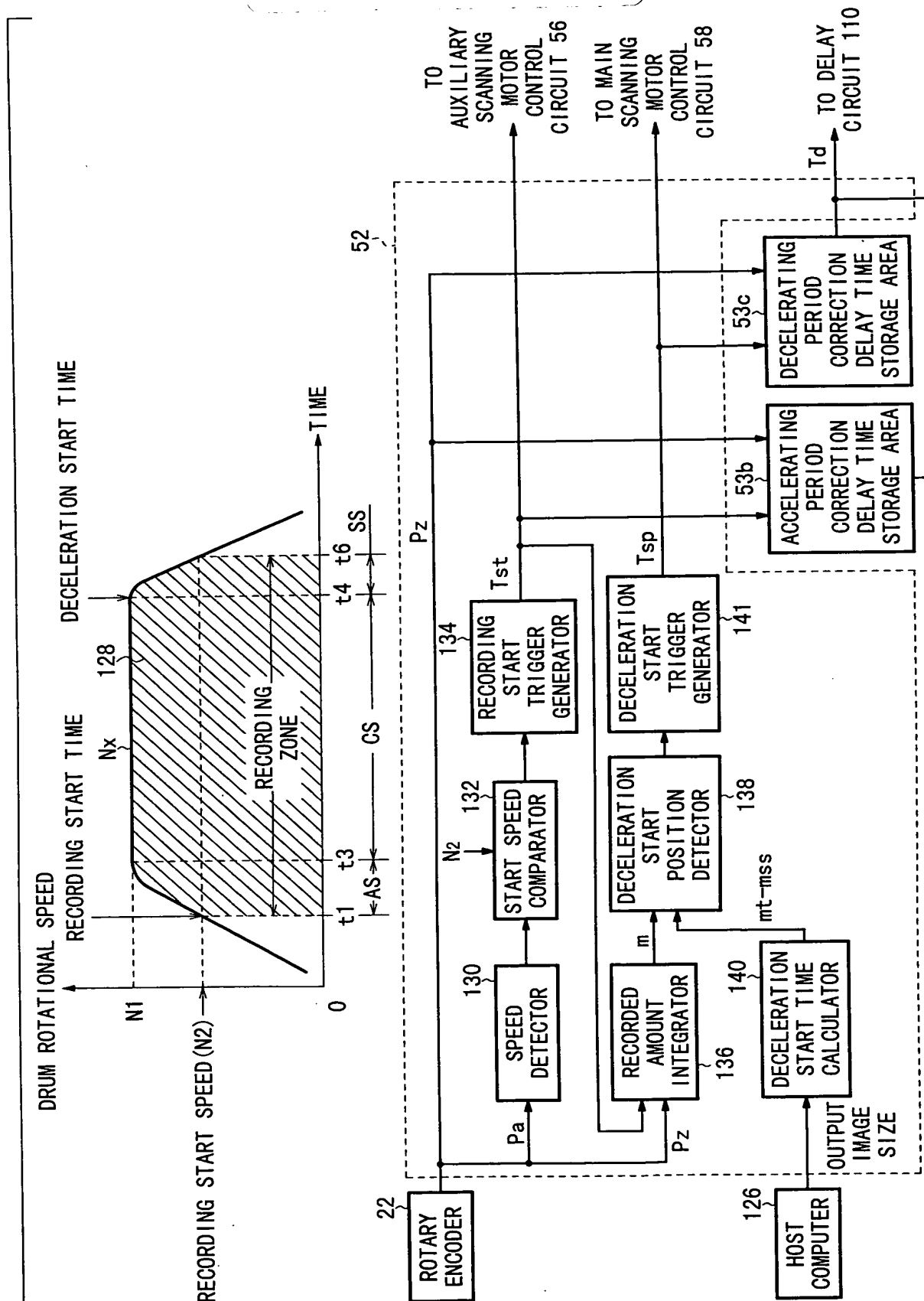
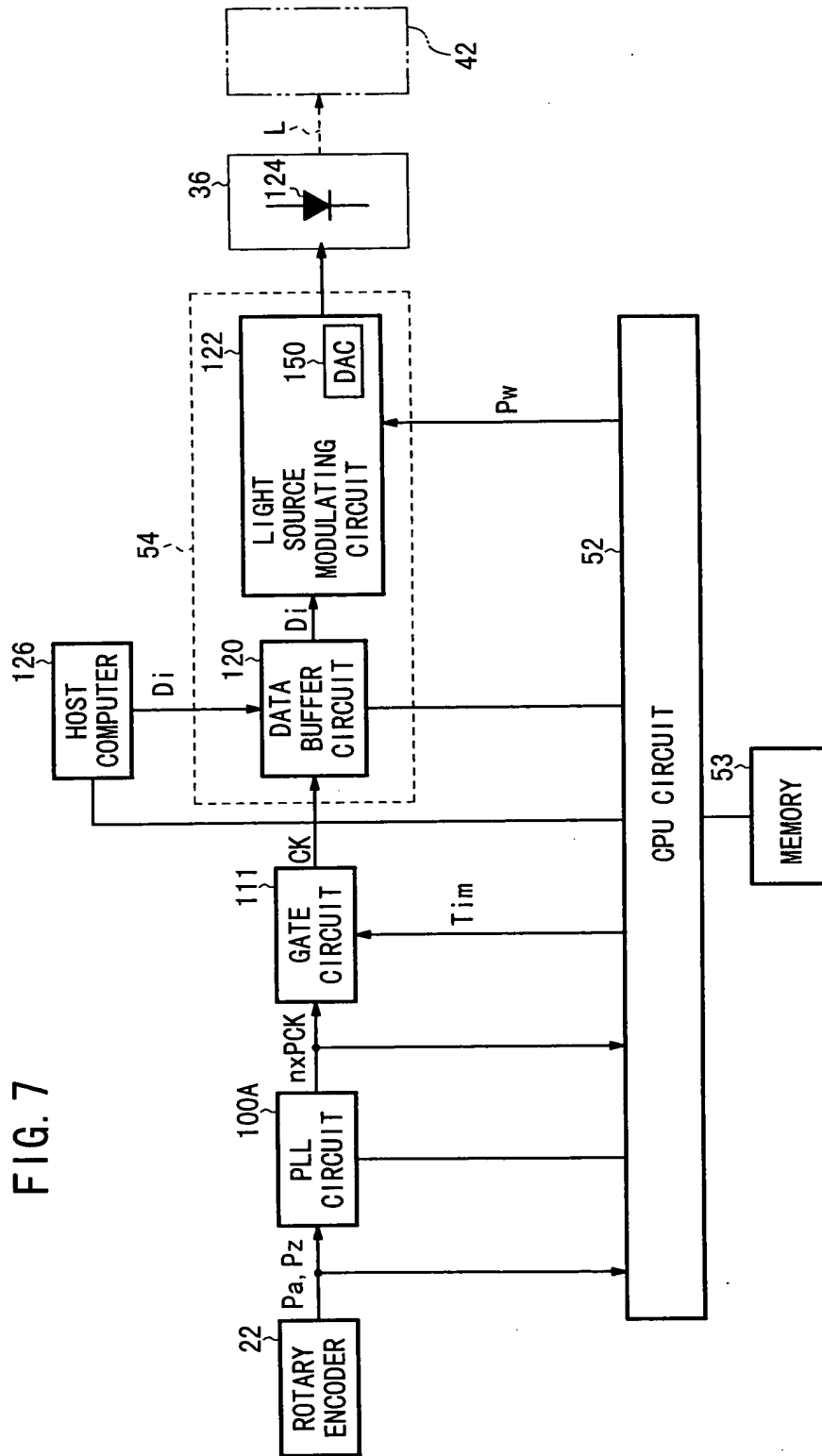


FIG. 7



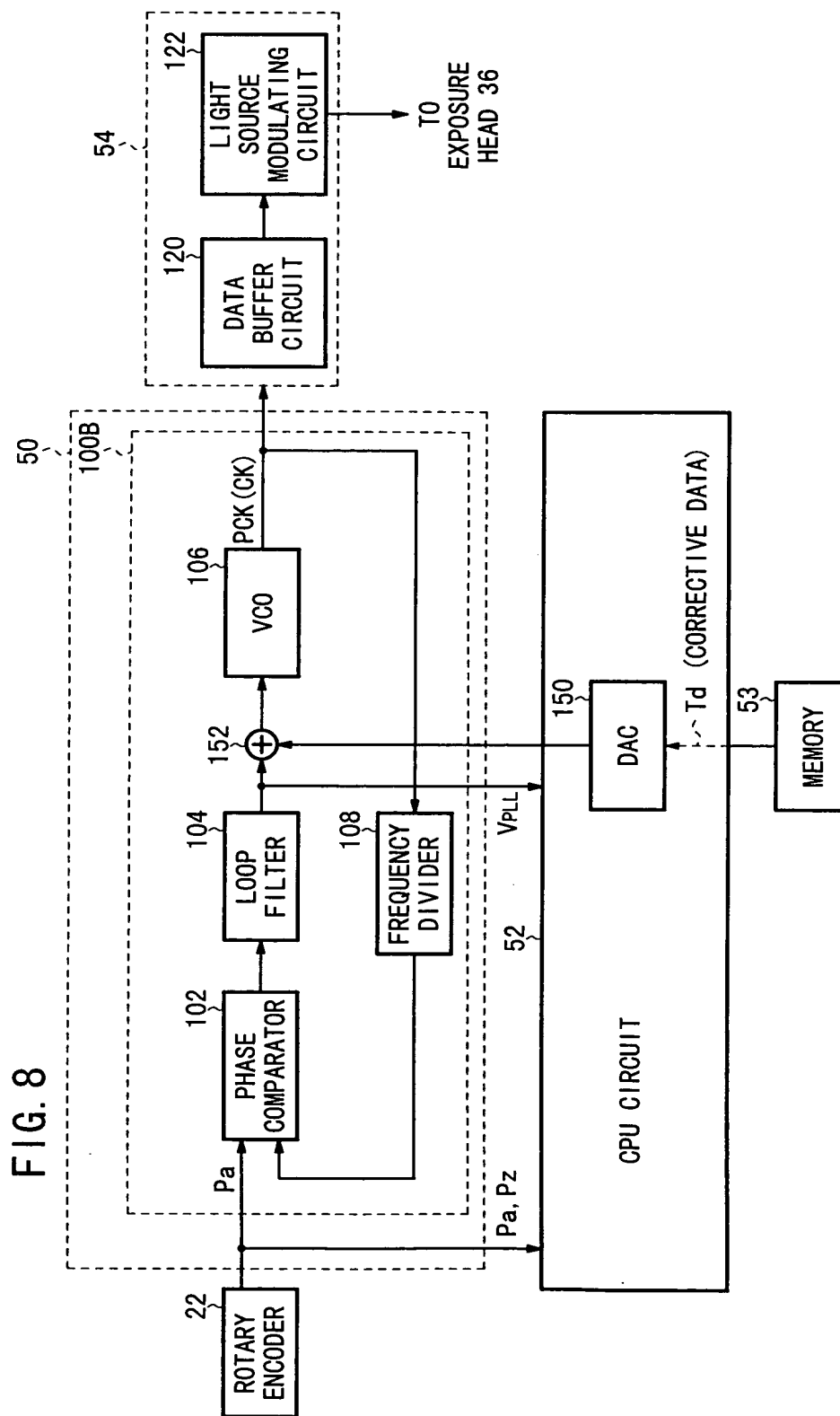




FIG. 9A

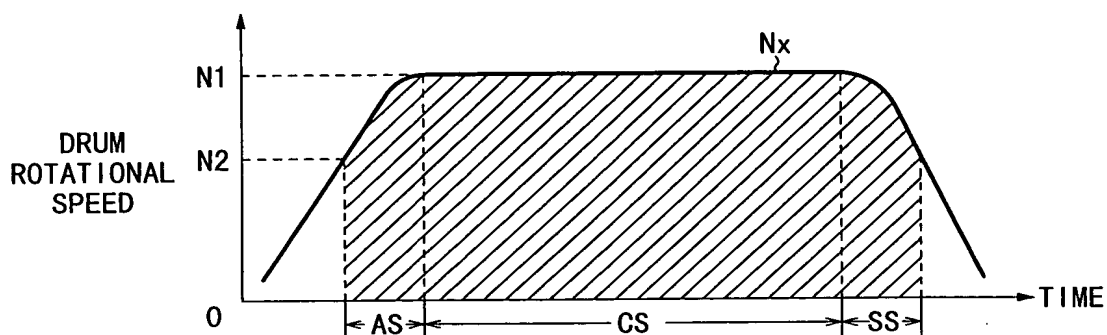
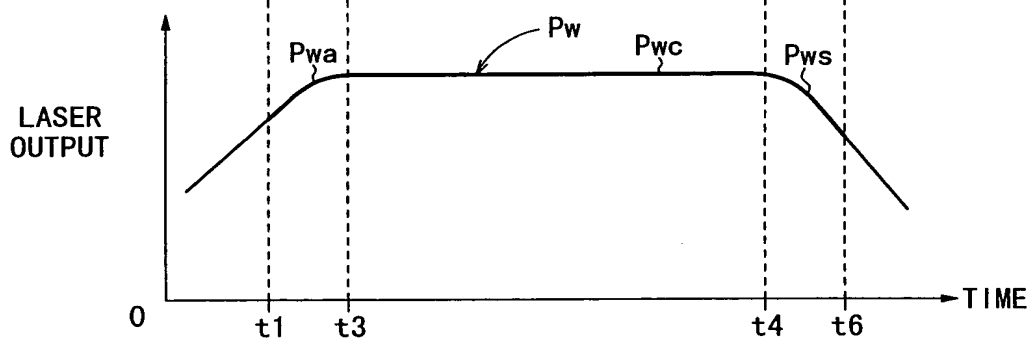


FIG. 9B



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FIG. 10

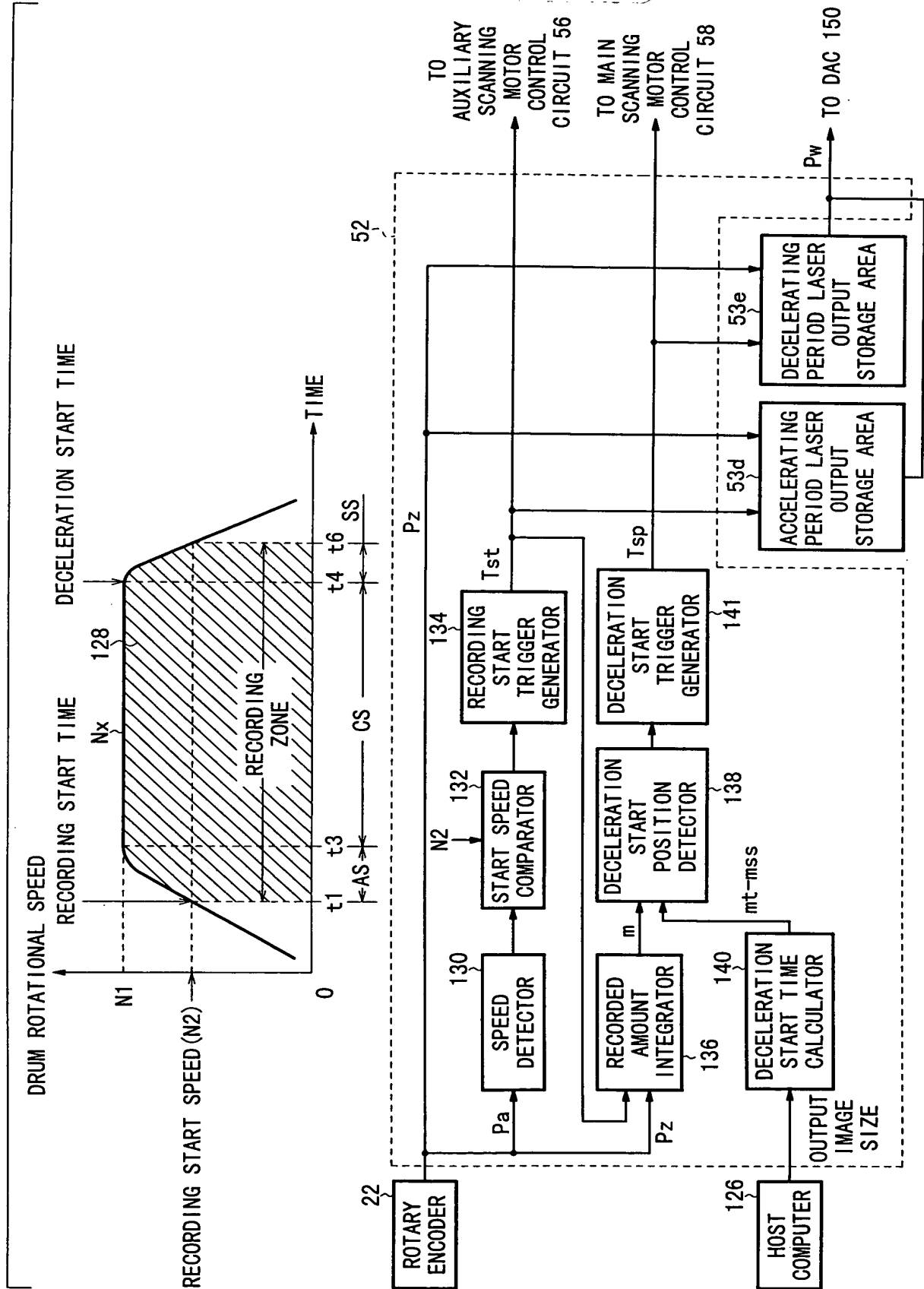


FIG. 11

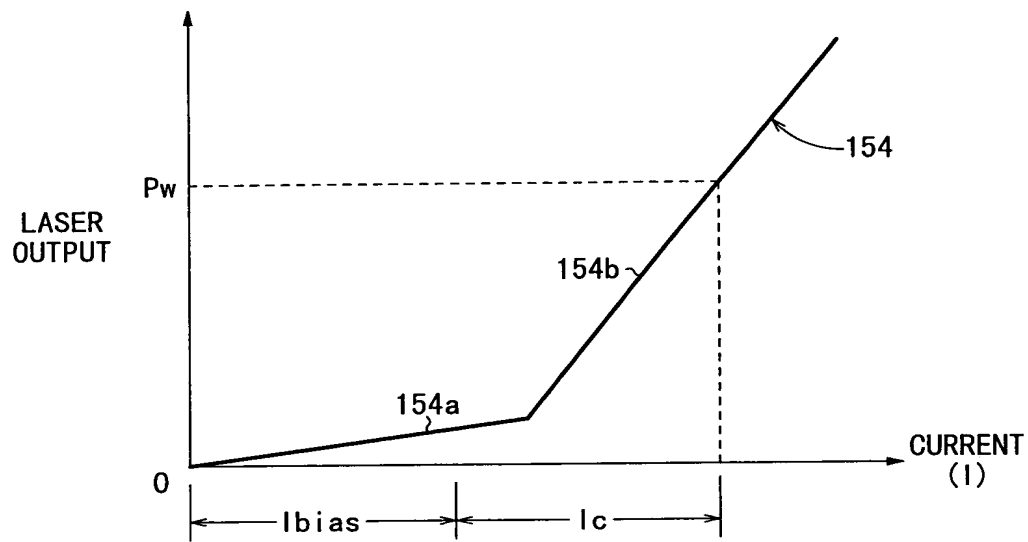


FIG. 12

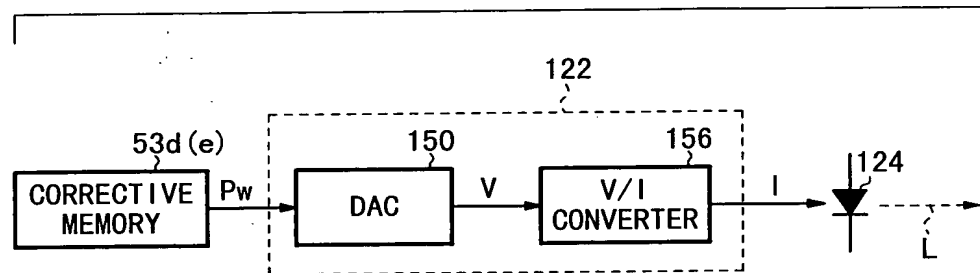


FIG. 13

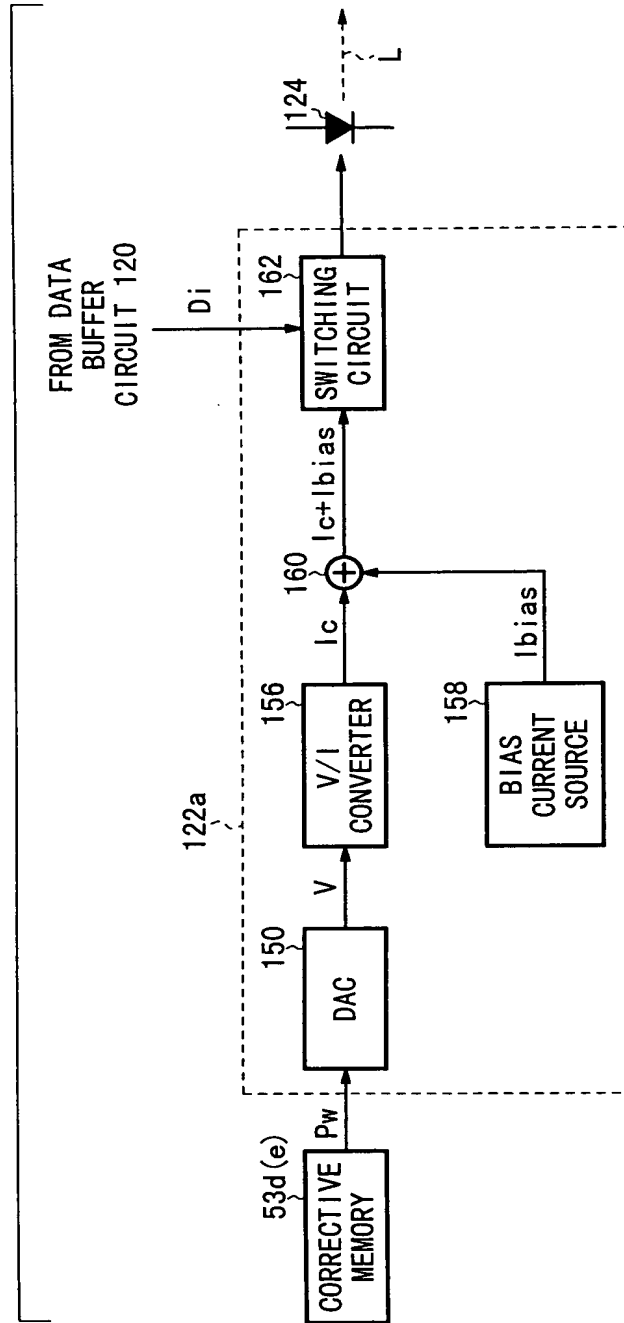


FIG. 14

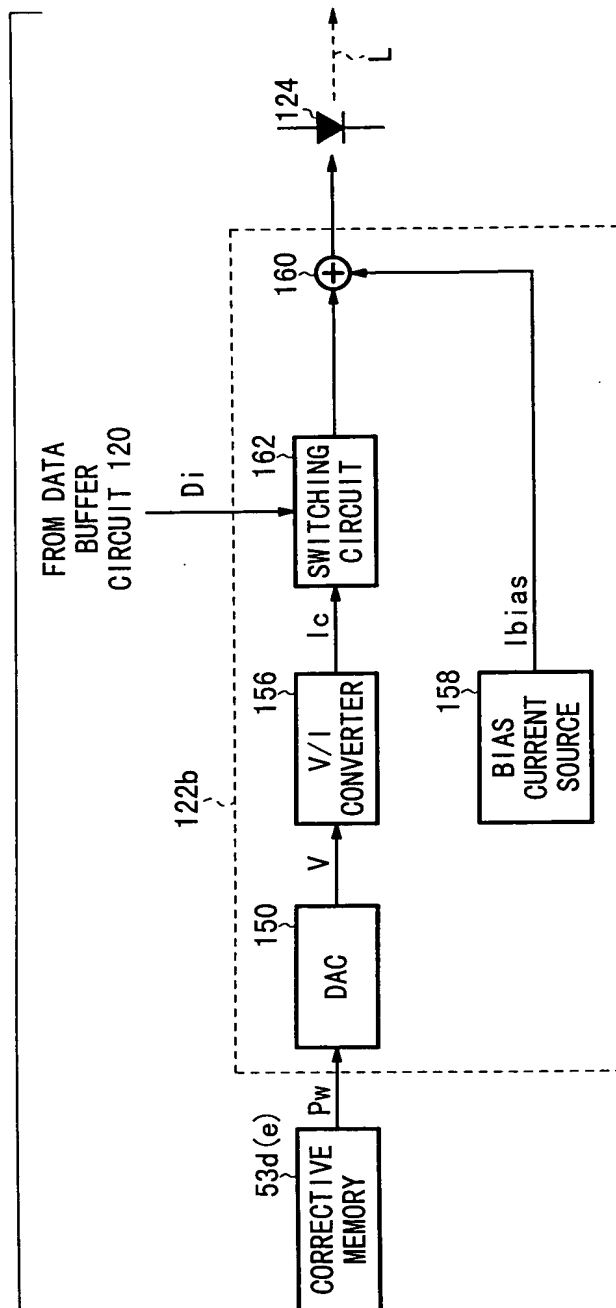
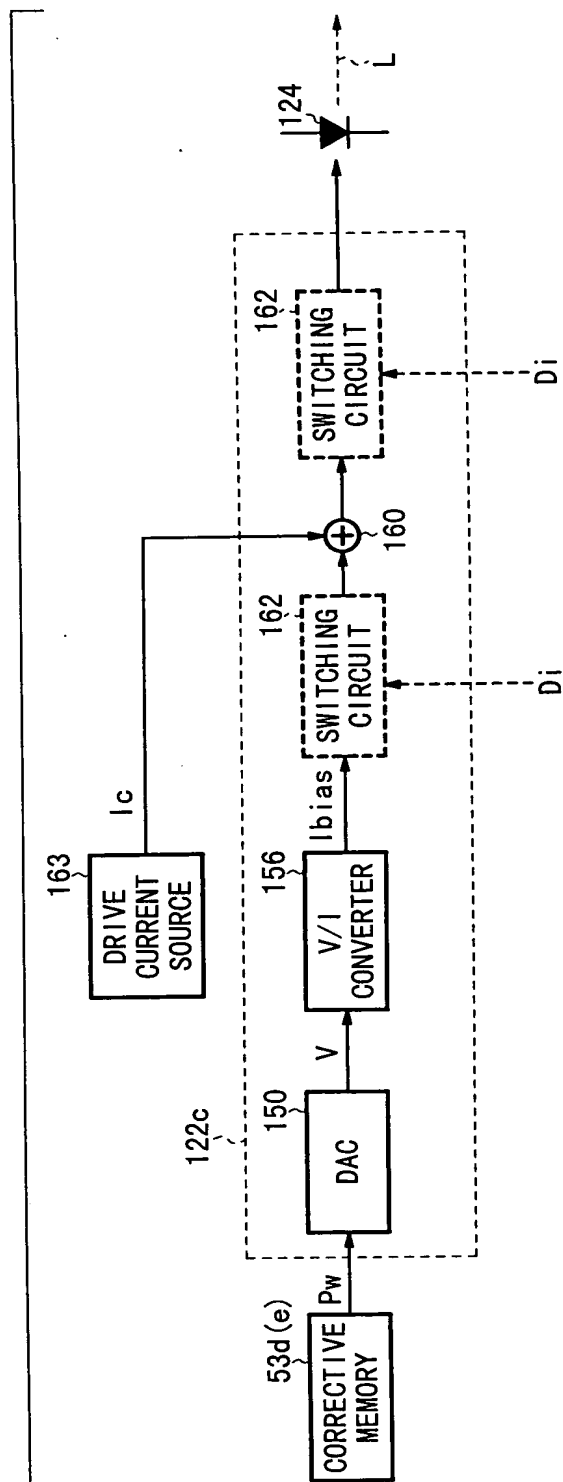


FIG. 15



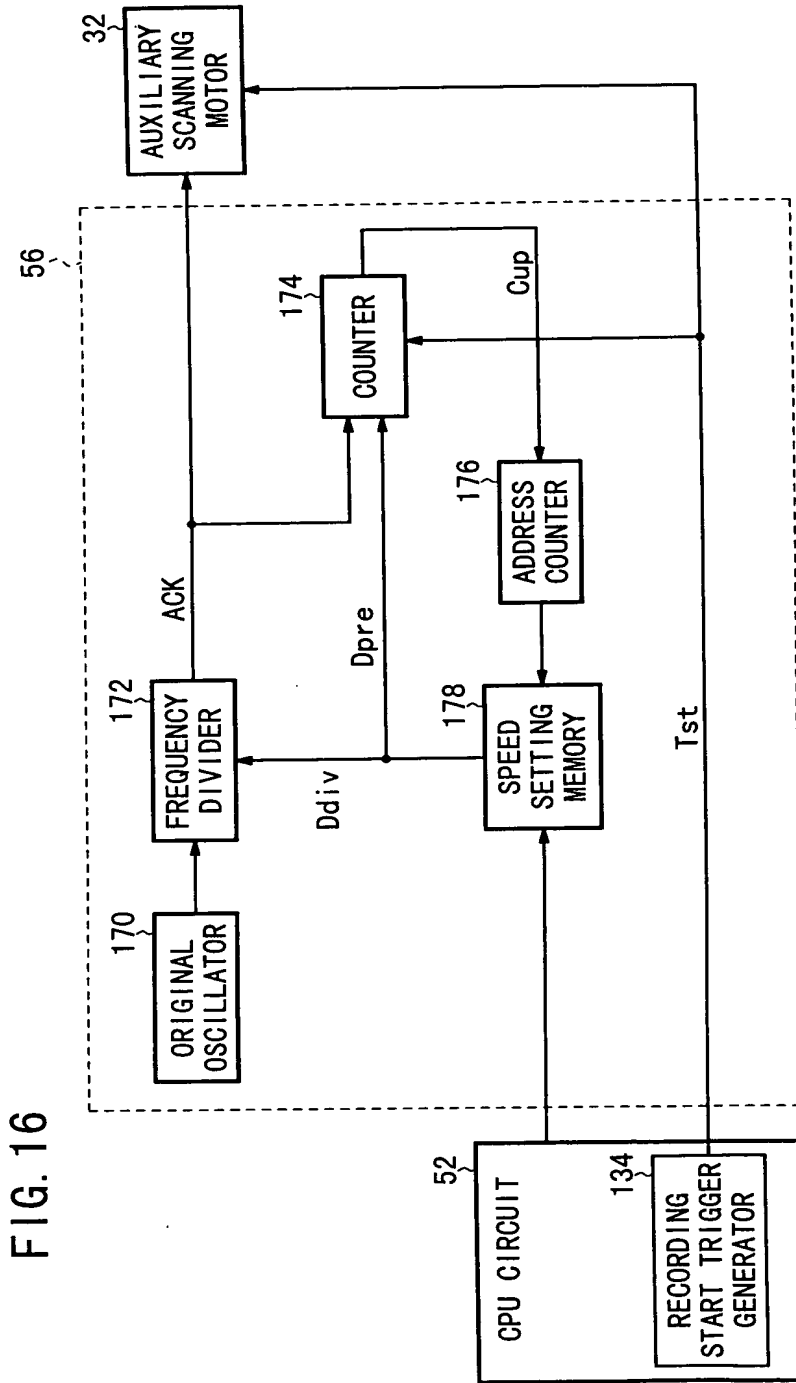


FIG. 16

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FIG. 17

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ADDRESS	FREQUENCY-DIVIDING RATIO (Ddiv)	PRESET VALUE (Dpre)	APPARATUS OPERATING STATUS
1	100	50	ACCELERATION STARTED
2	95	48	IN ACCELERATION
3	90	46	DITTO
4	85	44	DITTO
5	80	1000	AT CONSTANT SPEED
6	85	44	DECELERATION STARTED
7	90	46	IN DECELERATION
8	95	48	DITTO
9	100	50	DECELERATION ENDED

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FIG. 18

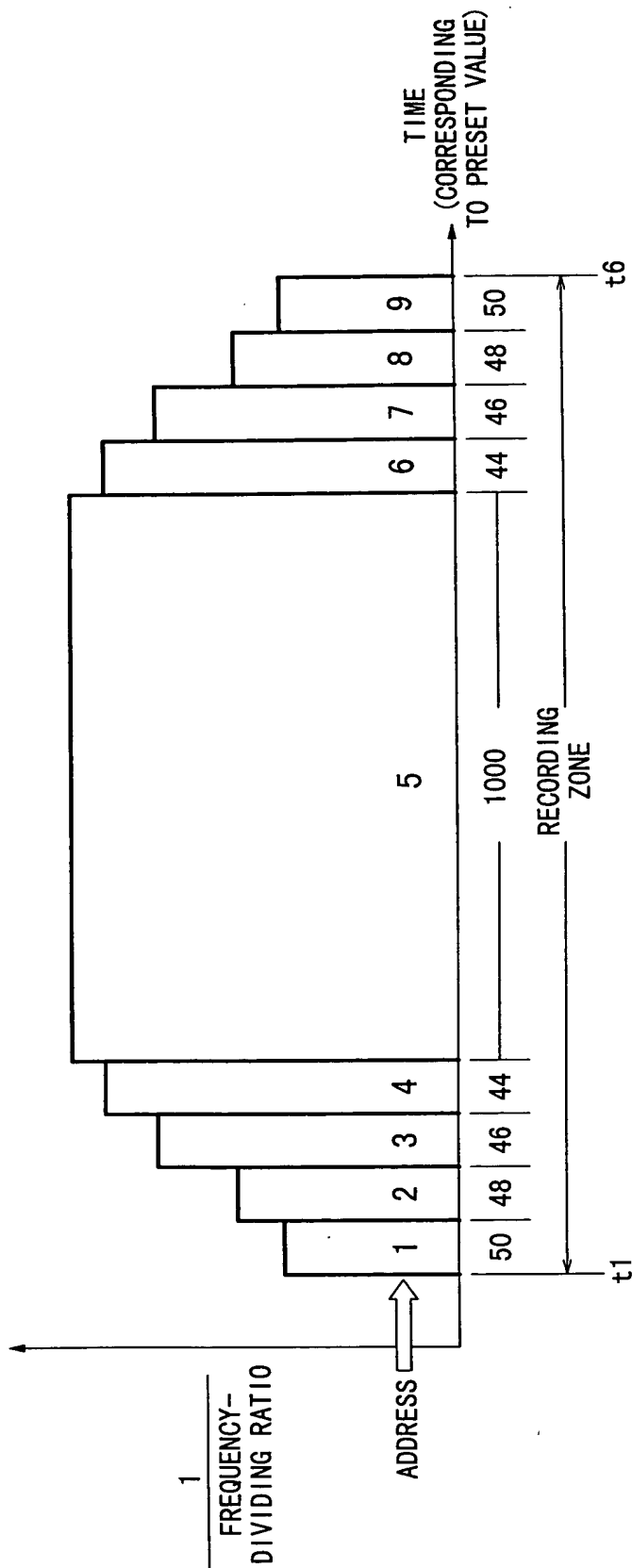


FIG. 19

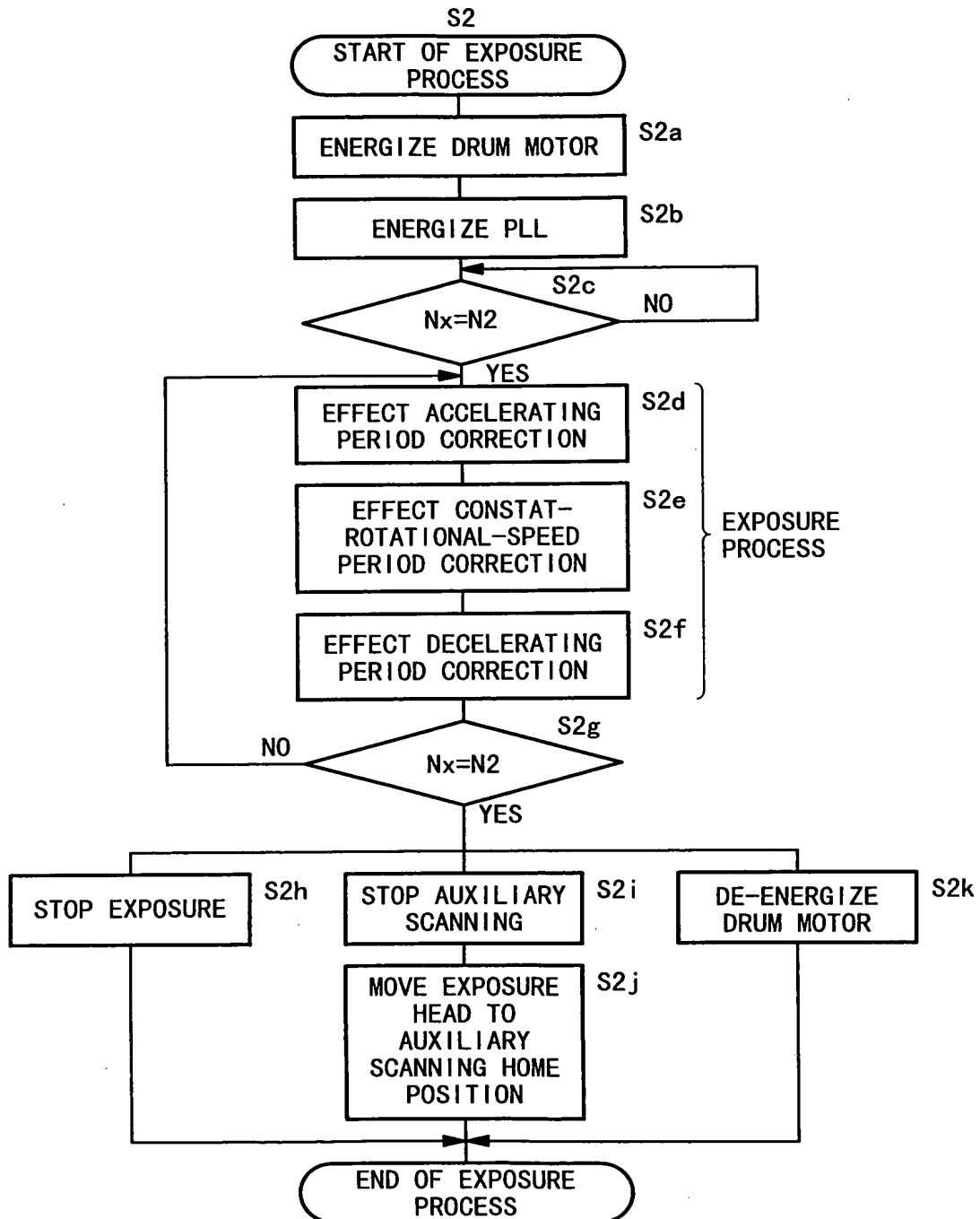
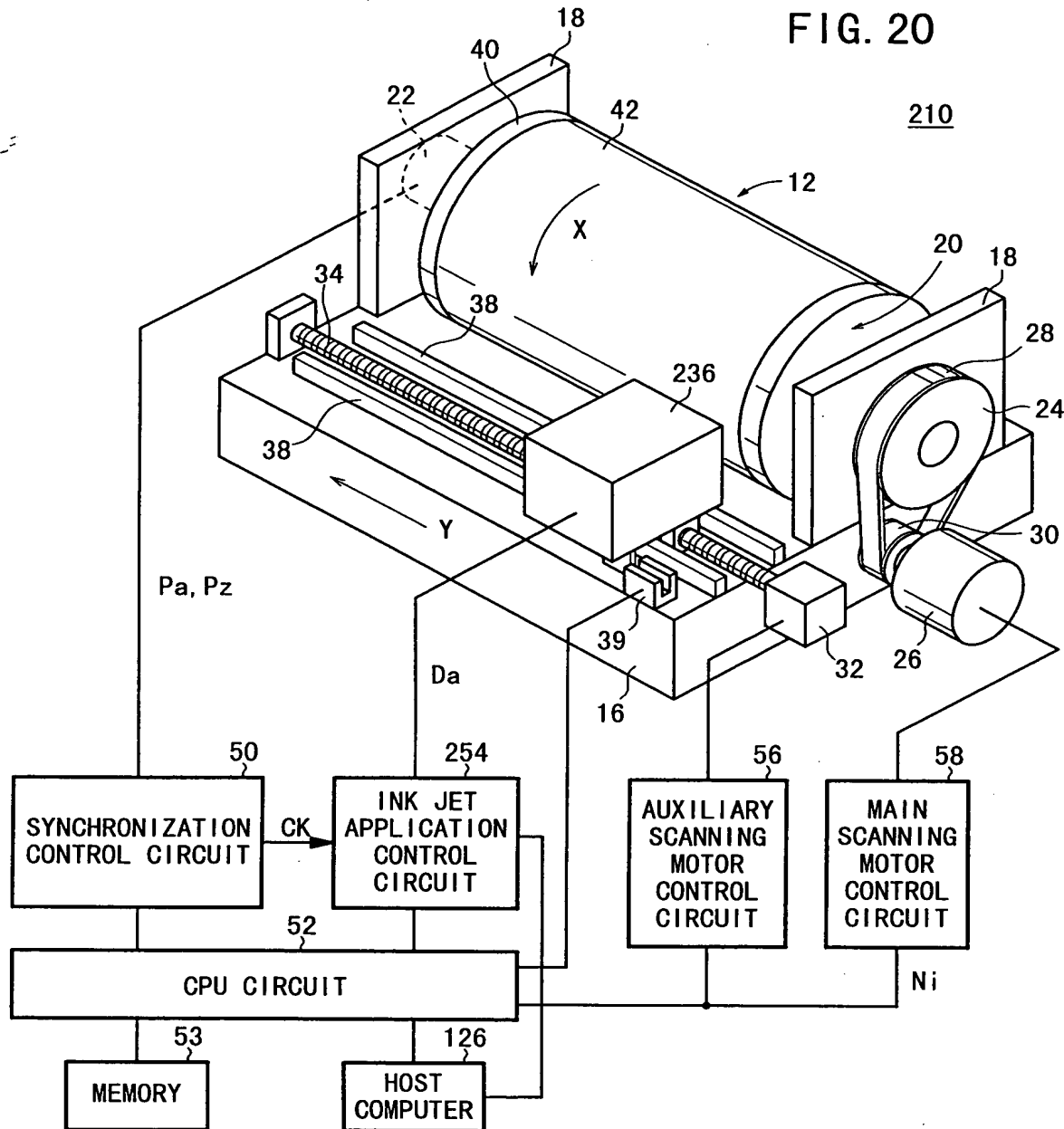
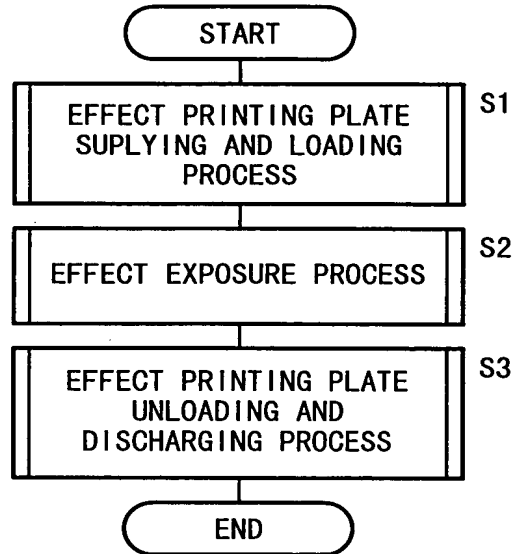


FIG. 20



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FIG. 21  
PRIOR ART



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FIG. 22  
PRIOR ART

